# **USB 3.0 ENGINEERING CHANGE NOTICE**

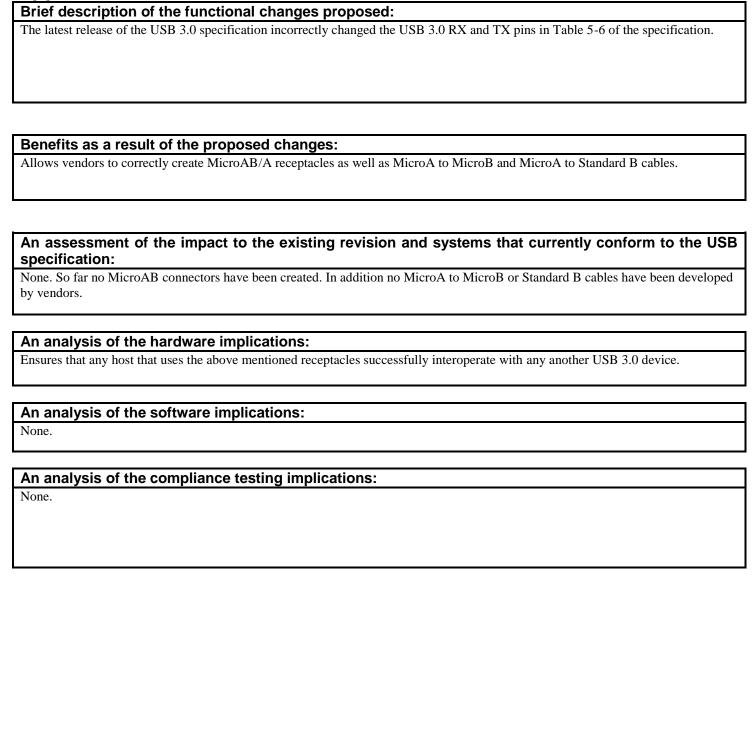
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#### **USB 3.0 ENGINEERING CHANGE NOTICE**

Title: USB3	.0 Micr	oAB	Cor	nec	ctor
Applied to:	USB3_	r1.0_	_06_	_06_	_2011



### **USB 3.0 ENGINEERING CHANGE NOTICE**

# **Actual Change Requested**

### (a). From Table (and location): Table 5-6, Page 5-34

Table 5-6. USB 3.0 Micro-AB/-A Connector Pin Assignments

Pin Number	Signal Name	Description	Mating Sequence	
1	VBUS	Power	Second	
2	D-	USB 2.0 differential pair	Last	
3	D+			
4	ID	OTG identification		
5	GND	Ground for power return	Second	
6	MicA_SSRX-	Super Speed receiver differential	last	
7	MicA_SSRX+	pair		
8	GND	Ground for SS signal return	Second	
9	MicA_SSTX-	Super Speed transmitter	Last	
10	MicA_SSTX+	differential pair		
Shell	Shield	Connector metal shell	First	

Note: TX and RX are defined when an OTG device serves as a host.

### To Table (and location): Table 5-6, Page 5-34

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