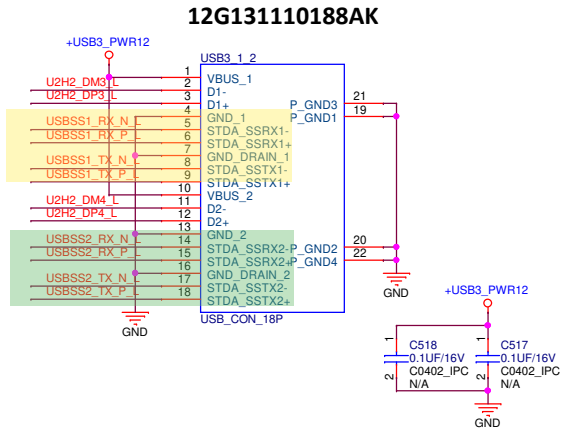
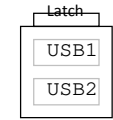
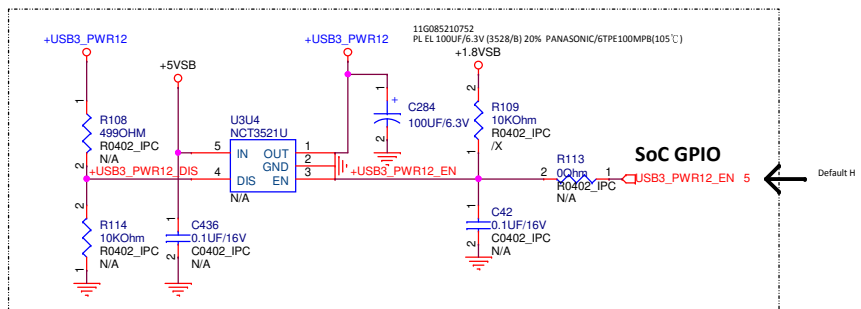
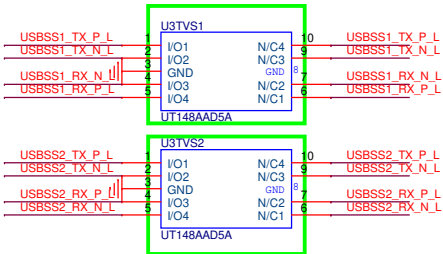
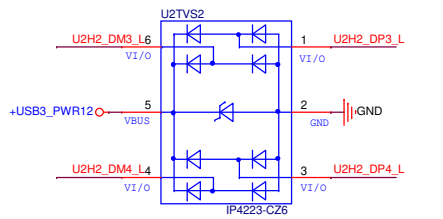
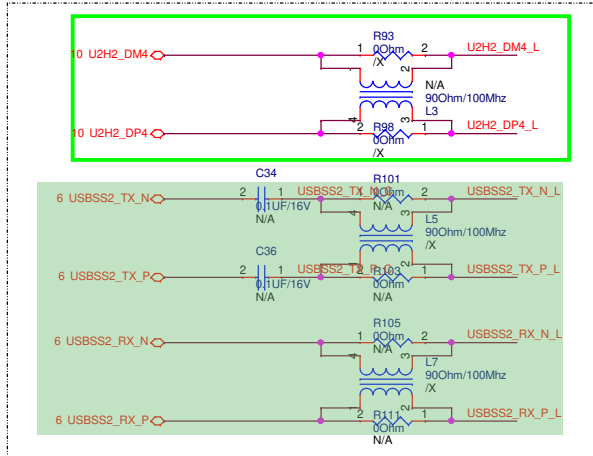
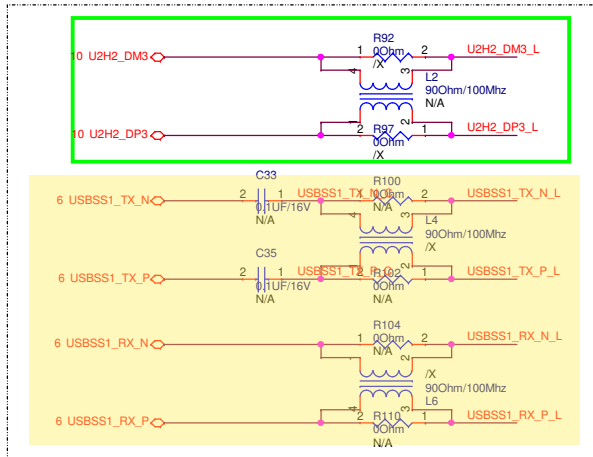
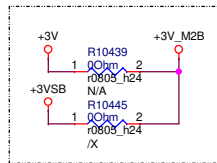


For Root Port operation, the mux should be set to output the SF_PCIE0_CLK signals. GP21 which is used for the mux select should be set low.





Refer intel KEY-B #723877

M.2 B-Key (PCIe / USB3.1)

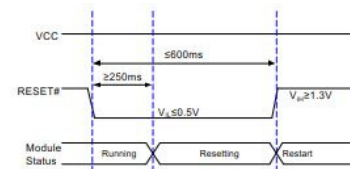
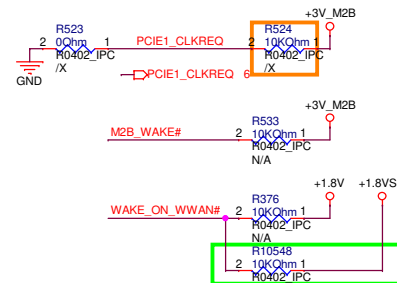
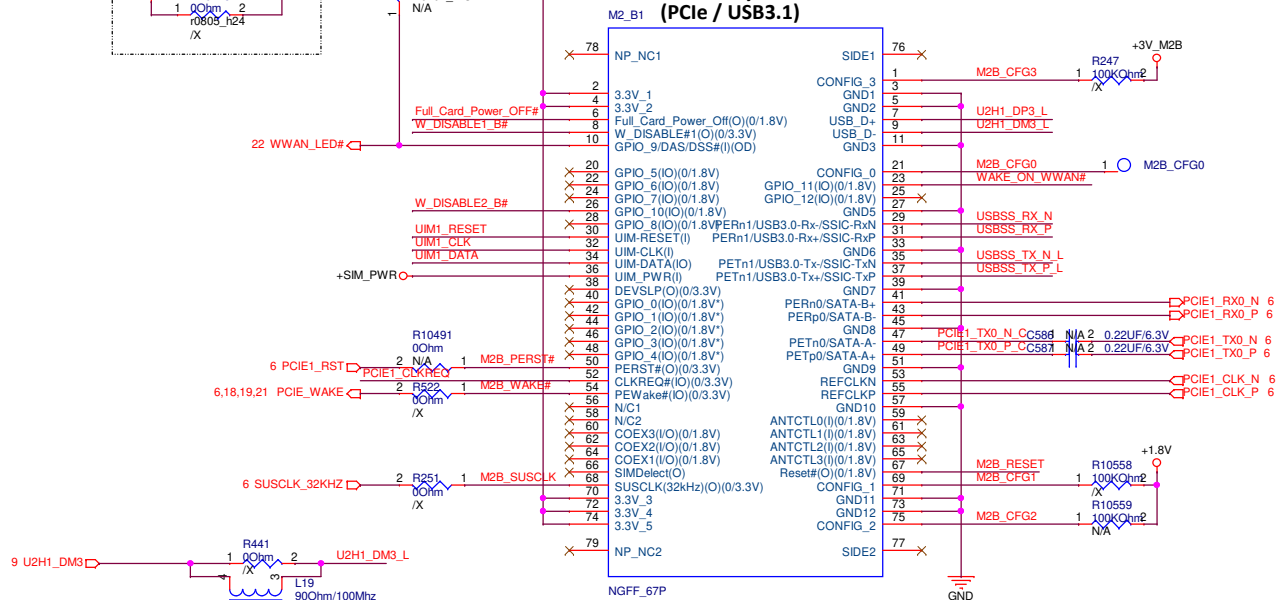
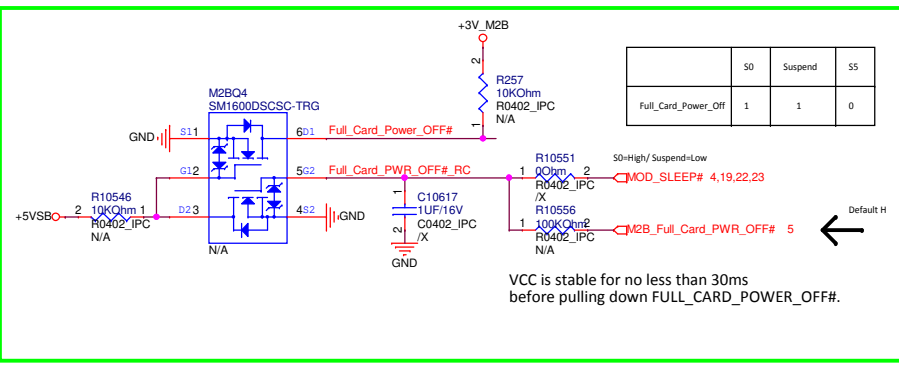
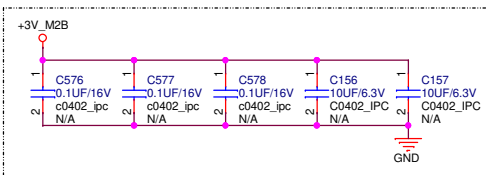
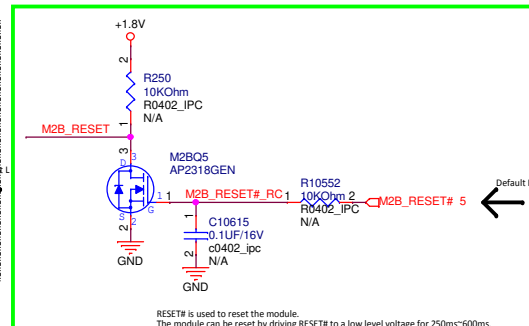
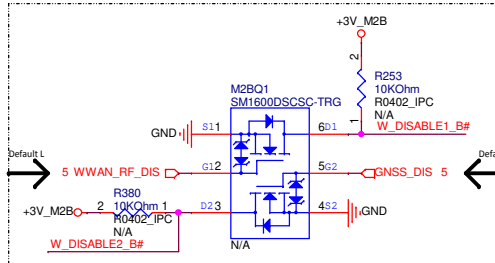
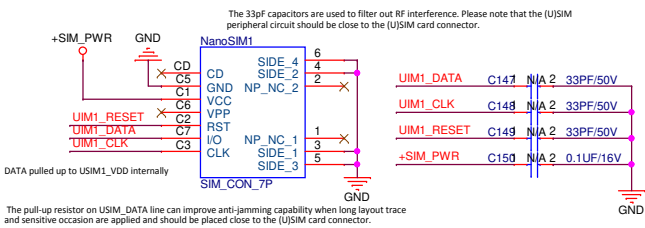
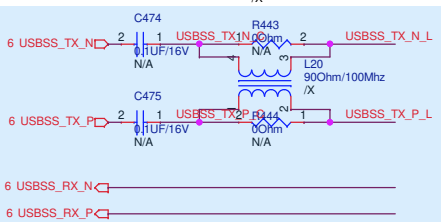


Figure 12: Timing of Resetting Module



	S0	Suspend	S5
Full_Card_Power_Off	1	1	0

S0=High/ Suspend=Low
 MOD_SLEEP# 4,19,22,23
 M2B_Full_Card_Pwr_Off# 5

VCC is stable for no less than 30ms before pulling down FULL_CARD_POWER_OFF#.

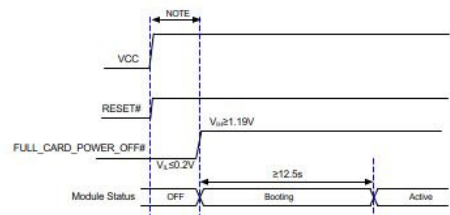
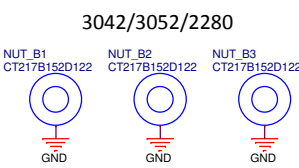


Figure 8: Timing of Turning on Module



ASRock Industrial

Title: 017-M2 B+Nano SIM

Size: Custom	Project Name: DSC-NV002-WT	Engineer: Charlie Huang	Rev: R1.01
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